OIPE COST

Ref: Inventor: Title: TN-264 Collier, et al.

Maintenance Interface Unit for Servicing Multiprocessor Systems

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500 502 Communicate with a Cache Coherent Multiprocessor System via Backdoor Access Memory Associated With 504 Multiprocessor System While Simultaneously Participating In Cache Coherency of the System 506 Issue Commands to a Dedicated Backdoor Interface Integrated Within the Multiprocessor System Receive Information from the 508 Backdoor Interface Pertaining to Error Handling, Platform Management, Operating System Performance and Other Parameters 510 Display Information Associated with Servicing and Maintaining the Multiprocessor System

Fig. 5